



Users Manual

TopDown Baseboard for Zefant XS3 FPGA Module

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2 Introduction

The TopDown baseboard is a simple evaluation platform for the Zefant FPGA module. It is mounted "bottom-up" on the baseboard for easy access to the SODIMM connector during evaluation. Placement of I/O-connectors allows mounting of standard breadboard PCB and their pinout is directly compatible with HP16500 logic analyzer termination adapters (01650-63203). The board was designed to fit perfectly into APW Veronex enclosure, which eases creating quick prototypes with professional appearance.

3 Quick Start

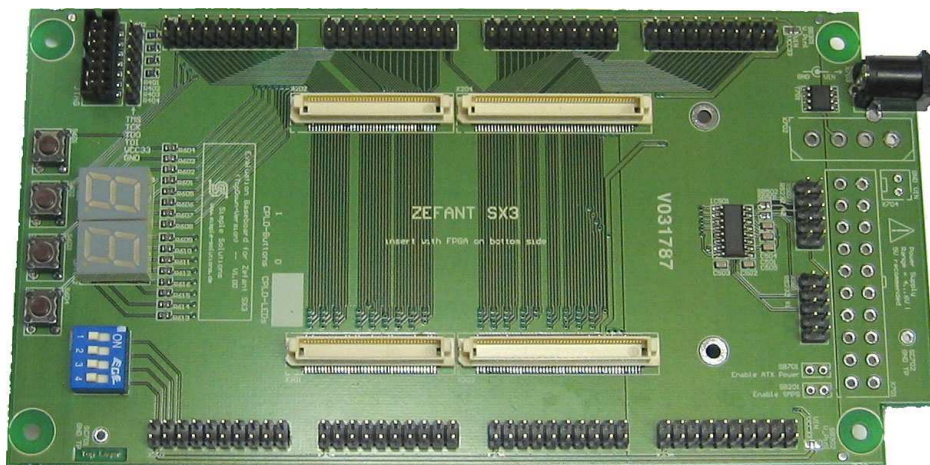
3.1 Checklist before PowerUp

Check that SB201 or SB202 are set to enable the power supply of the Zefant module.

Do also take note of the checklist in the Zefant module users manual.

4 Hardware Description

4.1 PCB top view



4.2 Solder bridge configuration

There are 2 solder bridges on the module for power supply configuration:

4.2.1 SB201/SB202

When set, the internal power supply of the Zefant module is enabled and the system powered up. Both jumpers are connected in parallel.

4.2.2 SB701/SB702

If an ATX power supply is connected to X701, closing one of these solder bridges powers it up. Both solder bridges are connected in parallel.

4.2.3 SB501/SB502

By default two RS232 interfaces are supported without handshaking on connectors X501 and X502. However, if you want to use RTS/CTS handshaking, close solder bridges SB501 and SB502 to connect RTS and CTS pins of X501 to the TXD/RXD lines of X502, which cannot be used anymore then.

5 Signal Descriptions

5.1 Signal reference

A signal cross reference between the pins of all active components and connectors is available as an Excel sheet, PDF printout and in basic UCF files on

<http://www.simple-solutions.de/en/support/download/>

Please refer to these resources for complete and most recent informations.

6 Version History

Datum	Version	Hinweise
2005-05-18	V1.2	Removed signal reference due to redundancy Please refer to the resources mentioned in ch. 5.1 instead.
2005-05-12	V1.1	Added Contents, corrected Signal reference (marked yellow)
2005-03-15	V1.0	Initial Release