

Zefant XS3 Pin/Signal Reference V1.4 (2005-08-11)

Signal Name	FPGA Pin	FPGA LVDS-Pair	Diff. Pair?	CPLD Pin	FX8C Connectors		Top (FPGA) Side Pin	SODIMM Connector Pin	TopDown Baseboard		Notes
					Bottom (SODIMM) Side	Pin			Connector	Pin	
GND					X1301		1 X1401	1			
GND					X1301		2 X1401	2			
FPGA_IO38	K21	2_35N	X		X1301		3 X1401	3	X304		18
FPGA_IO37	K19	2_34N	X		X1301		4 X1401	4	X303		18
FPGA_IO40	K22	2_35P	X		X1301		5 X1401	5	X304		19
FPGA_IO39	K20	2_34P	X		X1301		6 X1401	6	X303		19
FPGA_IO_DOWN6	J22	2_32P			X1301		7				corrected on 2005-07-28
FPGA_IO_DOWN7	K18	2_33P			X1301		8				corrected on 2005-07-28
FPGA_IO_DOWN8	H22	2_29P			X1301		9				corrected on 2005-07-28
FPGA_IO_DOWN9	J19	2_31P			X1301		10				corrected on 2005-07-28
FPGA_IO_DOWN10	G20	2_26N			X1301		11				
FPGA_IO_DOWN11	J17	2_28P			X1301		12				
FPGA_IO42	G19	2_23P			X1301		13 X1401	13	X306		5
FPGA_IO41	F18	2_19P			X1301		14 X1401	14	X305		5
FPGA_IO44	F20	2_24N			X1301		15 X1401	15	X306		6
FPGA_IO43	E20	2_20P			X1301		16 X1401	16	X305		6
FPGA_IO46	G18	2_22P			X1301		17 X1401	17	X306		7
FPGA_IO45	D20	2_16N			X1301		18 X1401	18	X305		7
FPGA_IO48	F19	2_23N			X1301		19 X1401	19	X306		8
FPGA_IO47	E19	2_20N			X1301		20 X1401	20	X305		8
FPGA_IO50	G21	2_27N	X		X1301		21 X1401	21	X306		9
FPGA_IO49	D19	2_16P			X1301		22 X1401	22	X305		9
FPGA_IO52	G22	2_27P	X		X1301		23 X1401	23	X306		10
FPGA_IO51	D18	1_09P			X1301		24 X1401	24	X305		10
FPGA_IO54	F21	2_24P			X1301		25 X1401	25	X306		11
FPGA_IO53	E18	2_19N			X1301		26 X1401	26	X305		11
GND					X1301		27 X1401	27			
GND					X1301		28 X1401	28			
FPGA_IO56	E21	2_21N			X1301		29 X1401	29	X306		12
FPGA_IO55	C18	1_09N			X1301		30 X1401	30	X305		12
FPGA_IO58	E22	2_21P			X1301		31 X1401	31	X306		13
FPGA_IO57	C20	2_01N			X1301		32 X1401	32	X305		13
FPGA_IO60	D21	2_17N	X		X1301		33 X1401	33	X306		14
FPGA_IO59	G17	2_22N			X1301		34 X1401	34	X305		14
FPGA_IO62	D22	2_17P	X		X1301		35 X1401	35	X306		15
FPGA_IO61	C19	1_01N			X1301		36 X1401	36	X305		15
FPGA_IO64	C21	2_01P			X1301		37 X1401	37	X306		16
FPGA_IO63	E17	1_15P			X1301		38 X1401	38	X305		16
FPGA_IO66	C22				X1301		39 X1401	39	X306		17
FPGA_IO65	F17				X1301		40 X1401	40	X305		17
VCCIO23					X1301		41				VCCIO23 only available on bottom side!
VCCIO23					X1301		42				VCCIO23 only available on bottom side!
VCCIO23					X1301		43				VCCIO23 only available on bottom side!
VCCIO23					X1301		44				VCCIO23 only available on bottom side!
FPGA_IO68	B20	1_01P			X1301		45 X1401	45	X306		18
FPGA_IO67	C17	1_16P			X1301		46 X1401	46	X305		18
FPGA_IO70	F16				X1301		47 X1401	47	X306		19
FPGA_IO69	E16				X1301		48 X1401	48	X305		19
FPGA_IO72	D17	1_15N			X1301		49 X1401	49	X308		2
FPGA_IO71	E15	1_24P			X1301		50 X1401	50	X307		2
FPGA_IO74	D15	1_24N			X1301		51 X1401	51	X308		3
FPGA_IO73	D13	1_29P			X1301		52 X1401	52	X307		3

GND				X1301	53	X1401	53		
GND				X1301	54	X1401	54		
FPGA_IO76	B19	1_06P	X	X1301	55	X1401	55	X308	4
FPGA_IO75	C13	1_29N		X1301	56	X1401	56	X307	4
FPGA_IO78	A19	1_06N	X	X1301	57	X1401	57	X308	5
FPGA_IO77	D14	1_27N		X1301	58	X1401	58	X307	5
FPGA_IO80	B18	1_10P	X	X1301	59	X1401	59	X308	6
FPGA_IO79	E14	1_27P		X1301	60	X1401	60	X307	6
FPGA_IO82	A18	1_10N	X	X1301	61	X1401	61	X308	7
FPGA_IO81	E13			X1301	62	X1401	62	X307	7
FPGA_IO84	B17	1_16N		X1301	63	X1401	63	X308	8
FPGA_IO83	F13			X1301	64	X1401	64	X307	8
FPGA_IO_DOWN12	A16	1_22N		X1301	65				
FPGA_IO_DOWN13	D16	1_19P		X1301	66				
FPGA_IO86	B15	1_25N	X	X1301	67	X1401	67	X308	10
FPGA_IO85	F12			X1301	68	X1401	68	X307	10
FPGA_IO88	A15	1_25P	X	X1301	69	X1401	69	X308	11
FPGA_IO87	E12	1_31P		X1301	70	X1401	70	X307	11
FPGA_IO90	B14	1_28P	X	X1301	71	X1401	71	X308	12
FPGA_IO89	B11	0_32N	X	X1301	72	X1401	72	X307	12
FPGA_IO92	A14	1_28N	X	X1301	73	X1401	73	X308	13
FPGA_IO91	A11	0_32P	X	X1301	74	X1401	74	X307	13
FPGA_IO94	B13	1_30P	X	X1301	75	X1401	75	X308	14
FPGA_IO93	C11	0_31P		X1301	76	X1401	76	X307	14
FPGA_IO96	A13	1_30N	X	X1301	77	X1401	77	X308	15
FPGA_IO95	D11	0_31N		X1301	78	X1401	78	X307	15
FPGA_IO_DOWN14	A12			X1301	79				
FPGA_IO_DOWN15	C12	1_32P		X1301	80				
GND				X1301	81	X1401	81		
GND				X1301	82	X1401	82		
GND				X1301	83	X1401	83		
GND				X1301	84	X1401	84		
GND				X1301	85	X1401	85		
GND				X1301	86	X1401	86		
GND				X1301	87	X1401	87		
GND				X1301	88	X1401	88		
GND				X1301	89	X1401	89		
GND				X1301	90	X1401	90		
VIN				X1301	91	X1401	91		
VIN				X1301	92	X1401	92		
VIN				X1301	93	X1401	93		
VIN				X1301	94	X1401	94		
VIN				X1301	95	X1401	95		
VIN				X1301	96	X1401	96		
VIN				X1301	97	X1401	97		
VIN				X1301	98	X1401	98		
VIN				X1301	99	X1401	99		
VIN				X1301	100	X1401	100		
GND				X1302	1	X1402	1		
GND				X1302	2	X1402	2		
SD_CKE0	K3			X1302	3			62	
SD_CLK0	L1			X1302	4			61	
SD_CAS#	T1			X1302	5			66	
n.c.				X1302	6				
SD_CKE1	G2			X1302	7			68	
SD_RAS#	K1			X1302	8			65	
SD_A12	H5			X1302	9			70	

SD_WE#	K2	X1302	10		67	
SD_A13	G5	X1302	11		72	
SD_CS0#	G1	X1302	12		69	
SD_CLK1	G6	X1302	13		74	
SD_CS1#	F5	X1302	14		71	
BYPASS22		X1302	15	X1402	15	no connection to internal circuitry
BYPASS23		X1302	16	X1402	16	no connection to internal circuitry
BYPASS24		X1302	17	X1402	17	no connection to internal circuitry
BYPASS25		X1302	18	X1402	18	no connection to internal circuitry
BYPASS26		X1302	19	X1402	19	no connection to internal circuitry
SD_DQ16	F2	X1302	20			83
SD_DQ48	F4	X1302	21			84
SD_DQ17	D1	X1302	22			85
SD_DQ49	F3	X1302	23			86
SD_DQ18	E1	X1302	24			87
SD_DQ50	E3	X1302	25			88
SD_DQ19	D2	X1302	26			89
GND		X1302	27	X1402	27	
GND		X1302	28	X1402	28	
SD_DQ51	E4	X1302	29			90
BYPASS27		X1302	30	X1402	30	no connection to internal circuitry
SD_DQ52	D4	X1302	31			94
SD_DQ20	C1	X1302	32			93
SD_DQ53	D3	X1302	33			96
SD_DQ21	E2	X1302	34			95
SD_DQ54	C4	X1302	35			98
SD_DQ22	C2	X1302	36			97
SD_DQ55	D5	X1302	37			100
SD_DQ23	C3	X1302	38			99
BYPASS28		X1302	39	X1402	39	no connection to internal circuitry
BYPASS29		X1302	40	X1402	40	no connection to internal circuitry
SD_A7	C5	X1302	41			104
SD_A6	E6	X1302	42			103
SD_BA0	A3	X1302	43			106
SD_A8	B4	X1302	44			105
n.c.		X1302	45			
SD_A9	A4	X1302	46			109
SD_BA1	F6	X1302	47			110
SD_A10	B5	X1302	48			111
SD_A11	C6	X1302	49			112
SD_DQM2	A5	X1302	50			115
n.c.		X1302	51			
SD_DQM3	B6	X1302	52			117
GND		X1302	53	X1402	53	
GND		X1302	54	X1402	54	
SD_DQM6	D6	X1302	55			116
SD_DQ24	D7	X1302	56			121
SD_DQM7	E7	X1302	57			118
SD_DQ25	E10	X1302	58			123
SD_DQ56	F7	X1302	59			122
SD_DQ26	F10	X1302	60			125
SD_DQ57	E9	X1302	61			124
SD_DQ27	C7	X1302	62			127
SD_DQ58	D9	X1302	63			126
SD_DQ28	A8	X1302	64			131
SD_DQ59	F9	X1302	65			128
SD_DQ29	C10	X1302	66			133

SD_DQ60	B8		X1302	67			132	
SD_DQ30	A10		X1302	68			135	
SD_DQ61	B9		X1302	69			134	
SD_DQ31	B10		X1302	70			137	
SD_DQ62	A9		X1302	71			136	
SD_SDA	D10		X1302	72			141	
SD_DQ63	F11		X1302	73			138	
n.c.			X1302	74				
SD_SCL	E11		X1302	75			142	
n.c.			X1302	76				
VCC33			X1302	77				
VCC33			X1302	78				
VCC33			X1302	79				
VCC33			X1302	80				
GND			X1302	81	X1402	81		
GND			X1302	82	X1402	82		
GND			X1302	83	X1402	83		
GND			X1302	84	X1402	84		
GND			X1302	85	X1402	85		
GND			X1302	86	X1402	86		
GND			X1302	87	X1402	87		
GND			X1302	88	X1402	88		
GND			X1302	89	X1402	89		
GND			X1302	90	X1402	90		
PSEN_33			X1302	91	X1402	91		
VIN			X1302	92	X1402	92		
VIN			X1302	93	X1402	93		
VIN			X1302	94	X1402	94		
VIN			X1302	95	X1402	95		
VIN			X1302	96	X1402	96		
VIN			X1302	97	X1402	97		
VIN			X1302	98	X1402	98		
VIN			X1302	99	X1402	99		
VIN			X1302	100	X1402	100		
GND			X1303	1	X1403	1		
GND			X1303	2	X1403	2		
CPLD_IO_DOWN0		M2	X1303	3				
CPLD_IO_DOWN1		L1	X1303	4				
CPLD_IO_DOWN2		K3	X1303	5				
CPLD_IO_DOWN3		N1	X1303	6				
CPLD_IO_DOWN4		L4	X1303	7				
CPLD_IO_DOWN5		M3	X1303	8				
CPLD_IO_DOWN6		K4	X1303	9				
CPLD_IO_DOWN7		N3	X1303	10				
CPLD_IO0		N4	X1303	11	X1403	11	X302	6
CPLD_IO1		M4	X1303	12	X1403	12	X301	6
CPLD_IO2		N6	X1303	13	X1403	13	X302	7
CPLD_IO3		K5	X1303	14	X1403	14	X301	7
CPLD_IO4		N7	X1303	15	X1403	15	X302	8
CPLD_IO5		L5	X1303	16	X1403	16	X301	8
CPLD_IO6		M7	X1303	17	X1403	17	X302	9
CPLD_IO7		M5	X1303	18	X1403	18	X301	9
CPLD_IO8		N10	X1303	19	X1403	19	X302	10
CPLD_IO9		M6	X1303	20	X1403	20	X301	10
CPLD_IO10		N11	X1303	21	X1403	21	X302	11
CPLD_IO11		M10	X1303	22	X1403	22	X301	11
CPLD_IO12		M11	X1303	23	X1403	23	X302	12

CPLD_IO13			L10	X1303	24	X1403	24	X301	12
CPLD_IO14			M12	X1303	25	X1403	25	X302	13
CPLD_IO15			L11	X1303	26	X1403	26	X301	13
GND				X1303	27	X1403	27		
GND				X1303	28	X1403	28		
JTAG_TD_BYPASS				X1303	29	X1403	29		
JTAG_TDI				X1303	30				
JTAG_TMS				X1303	31	X1403	31		
JTAG_TCK				X1303	32	X1403	32		
FPGA_IO0	U19	3_20N		X1303	33	X1403	33	X302	14
CPLD_CLK	Y11		A7	X1303	34	X1403	34		
FPGA_IO2	W19	3_17P		X1303	35	X1403	35	X302	15
FPGA_IO1	T17	3_22N		X1303	36	X1403	36	X301	14
FPGA_IO4	V19	3_17N		X1303	37	X1403	37	X302	16
FPGA_IO3	M17	3_38P		X1303	38	X1403	38	X301	15
FPGA_IO6	U20	3_23P		X1303	39	X1403	39	X302	17
FPGA_IO5	T18	3_24P		X1303	40	X1403	40	X301	16
FPGA_IO8	W20	3_19P		X1303	41	X1403	41	X302	18
FPGA_IO7	Y19	3_01P		X1303	42	X1403	42	X301	17
FPGA_IO10	Y21			X1303	43	X1403	43	X302	19
FPGA_IO9	U18	3_22P		X1303	44	X1403	44	X301	18
FPGA_IO12	W21	3_19N		X1303	45	X1403	45	X304	2
FPGA_IO11	R18	3_24N		X1303	46	X1403	46	X301	19
FPGA_IO14	Y22	3_16P		X1303	47	X1403	47	X304	3
FPGA_IO13	Y20	3_01N		X1303	48	X1403	48	X303	2
FPGA_IO16	V20	3_20P		X1303	49	X1403	49	X304	4
FPGA_IO15	V21	3_21P		X1303	50	X1403	50	X303	4
FPGA_IO18	W22	3_16N		X1303	51	X1403	51	X304	5
FPGA_IO17	V22	3_21N		X1303	52	X1403	52	X303	5
GND				X1303	53	X1403	53		
GND				X1303	54	X1403	54		
FPGA_IO20	U21	3_23N		X1303	55	X1403	55	X304	6
FPGA_IO_DOWN1	N17	3_33P		X1303	56				
FPGA_IO_DOWN0	R19	3_29P		X1303	57				
FPGA_IO_DOWN3	T19	3_26P		X1303	58				
FPGA_IO_DOWN2	P18	3_31N		X1303	59				
FPGA_IO_DOWN5	P19	3_29N		X1303	60				
FPGA_IO_DOWN4	N18	3_33N		X1303	61				
FPGA_IO19	M20	3_39N		X1303	62	X1403	62	X303	9
FPGA_IO22	N20	3_34N		X1303	63	X1403	63	X304	10
FPGA_IO21	M22	3_40N		X1303	64	X1403	64	X303	10
FPGA_IO24	N21	3_35P		X1303	65	X1403	65	X304	11
FPGA_IO23	L21	2_40N		X1303	66	X1403	66	X303	11
FPGA_IO26	N19	3_34P		X1303	67	X1403	67	X304	12
FPGA_IO25	L22	2_40P		X1303	68	X1403	68	X303	12
FPGA_IO28	L17	2_38N		X1303	69	X1403	69	X304	13
FPGA_IO27	M18	3_38N		X1303	70	X1403	70	X303	13
FPGA_IO30	T21	3_27P		X1303	71	X1403	71	X304	14
FPGA_IO29	M19	3_39P		X1303	72	X1403	72	X303	14
FPGA_IO32	N22	3_35N		X1303	73	X1403	73	X304	15
FPGA_IO31	L20	2_39P		X1303	74	X1403	74	X303	15
FPGA_IO34	T22	3_27N		X1303	75	X1403	75	X304	16
FPGA_IO33	L19	2_39N		X1303	76	X1403	76	X303	16
FPGA_IO36	M21	3_40P		X1303	77	X1403	77	X304	17
FPGA_IO35	L18	2_38P		X1303	78	X1403	78	X303	17
GND				X1303	79	X1403	79		
GND				X1303	80	X1403	80		

corrected on 2005-07-28
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GND		X1304	1	X1404	1		
GND		X1304	2	X1404	2		
BYPASS0		X1304	3	X1404	3		no connection to internal circuitry
BYPASS1		X1304	4	X1404	4		no connection to internal circuitry
BYPASS2		X1304	5	X1404	5		no connection to internal circuitry
BYPASS3		X1304	6	X1404	6		no connection to internal circuitry
BYPASS4		X1304	7	X1404	7		no connection to internal circuitry
BYPASS5		X1304	8	X1404	8		no connection to internal circuitry
BYPASS6		X1304	9	X1404	9		no connection to internal circuitry
BYPASS7		X1304	10	X1404	10		no connection to internal circuitry
BYPASS8		X1304	11	X1404	11		no connection to internal circuitry
BYPASS9		X1304	12	X1404	12		no connection to internal circuitry
SD_DQ32	Y5	X1304	13			4	
SD_DQ0	AA4	X1304	14			3	
SD_DQ33	AA5	X1304	15			6	
SD_DQ1	AB4	X1304	16			5	
SD_DQ34	AB5	X1304	17			8	
SD_DQ2	W5	X1304	18			7	
SD_DQ35	Y2	X1304	19			10	
SD_DQ3	Y3	X1304	20			9	
FPGA_IO_DOWN16	G4	7_26P	21				
BYPASS11		X1304	22	X1404	22		no connection to internal circuitry
SD_DQ36	W4	X1304	23			14	
SD_DQ4	Y1	X1304	24			13	
SD_DQ37	W2	X1304	25			16	
SD_DQ5	W3	X1304	26			15	
GND		X1304	27	X1404	27		
GND		X1304	28	X1404	28		
SD_DQ38	U5	X1304	29			18	
SD_DQ6	W1	X1304	30			17	
SD_DQ39	V4	X1304	31			20	
SD_DQ7	V5	X1304	32			19	
FPGA_IO_DOWN17	H4	7_29P	33				
BYPASS13		X1304	34	X1404	34		no connection to internal circuitry
SD_DQM4	V2	X1304	35			24	
SD_DQM0	V3	X1304	36			23	
SD_DQM5	T6	X1304	37			26	
SD_DQM1	V1	X1304	38			25	
SD_A3	T4	X1304	39			30	
SD_A0	T5	X1304	40			29	
SD_A4	U3	X1304	41			32	
SD_A1	U4	X1304	42			31	
SD_A5	T2	X1304	43			34	
SD_A2	U2	X1304	44			33	
FPGA_IO_DOWN18	R5	6_28N	45				
FPGA_IO_DOWN19	R1	6_29P	46				
FPGA_IO_DOWN20	P4	6_31P	47				
SD_DQ8	N1	X1304	48			37	
FPGA_IO_DOWN21	N5	6_33P	49				
FPGA_IO_DOWN22	R4	6_26P	50				
SD_DQ40	N4	X1304	51			38	
FPGA_IO_DOWN23	P1	6_32P	52				
GND		X1304	53	X1404	53		
GND		X1304	54	X1404	54		
SD_DQ41	L5	X1304	55			40	
SD_DQ9	N3	X1304	56			39	
SD_DQ42	M6	X1304	57			42	

SD_DQ10	M2		X1304	58		41	
SD_DQ43	M4		X1304	59		44	
SD_DQ11	M5		X1304	60		43	
SD_DQ44	K4		X1304	61		48	
SD_DQ12	M3		X1304	62		47	
SD_DQ45	L3		X1304	63		50	
SD_DQ13	M1		X1304	64		49	
SD_DQ46	L4		X1304	65		52	
SD_DQ14	N2		X1304	66		51	
SD_DQ47	L6		X1304	67		54	
SD_DQ15	L2		X1304	68		53	
FPGA_IO_DOWN24	K6	7_33P	X1304	69			
BYPASS15			X1304	70	X1404	70	no connection to internal circuitry
FPGA_IO_DOWN25	J6	7_31P	X1304	71			
BYPASS17			X1304	72	X1404	72	no connection to internal circuitry
FPGA_IO_DOWN26	J2	7_32P	X1304	73			
BYPASS19			X1304	74	X1404	74	no connection to internal circuitry
FPGA_IO_DOWN27	H2	7_28P	X1304	75			
BYPASS21			X1304	76	X1404	76	no connection to internal circuitry
n.c.			X1304	77			
n.c.			X1304	78			
GND			X1304	79	X1404	79	
GND			X1304	80	X1404	80	
FPGA_IO_UP6	J21	2_32N	X1401	7		X306	2
FPGA_IO_UP7	K17	2_33N	X1401	8		X305	2
FPGA_IO_UP8	H21	2_29N	X1401	9		X306	3 corrected on 2005-07-28
FPGA_IO_UP9	J18	2_31N	X1401	10		X305	3
FPGA_IO_UP10	H19	2_26P	X1401	11		X306	4
FPGA_IO_UP11	H18	2_28N	X1401	12		X305	4
n.c.			X1401	41			VCCIO23 only available on bottom side!
n.c.			X1401	42			VCCIO23 only available on bottom side!
n.c.			X1401	43			VCCIO23 only available on bottom side!
n.c.			X1401	44			VCCIO23 only available on bottom side!
FPGA_IO_UP12	B16	1_22P	X1401	65		X308	9
FPGA_IO_UP13	C16	1_19N	X1401	66		X307	9
FPGA_IO_UP14	B12	1_32N	X1401	79		X308	16
FPGA_IO_UP15	D12	1_31N	X1401	80		X307	16
n.c.			X1402	3			
n.c.			X1402	4			
n.c.			X1402	5			
n.c.			X1402	6			
n.c.			X1402	7			
n.c.			X1402	8			
n.c.			X1402	9			
n.c.			X1402	10			
n.c.			X1402	11			
n.c.			X1402	12			
n.c.			X1402	13			
n.c.			X1402	14			
n.c.			X1402	20			
n.c.			X1402	21			
n.c.			X1402	22			
n.c.			X1402	23			
n.c.			X1402	24			
n.c.			X1402	25			
n.c.			X1402	26			
n.c.			X1402	29			

n.c.			X1402	31		
n.c.			X1402	32		
n.c.			X1402	33		
n.c.			X1402	34		
n.c.			X1402	35		
n.c.			X1402	36		
n.c.			X1402	37		
n.c.			X1402	38		
n.c.			X1402	41		
n.c.			X1402	42		
n.c.			X1402	43		
n.c.			X1402	44		
n.c.			X1402	45		
n.c.			X1402	46		
n.c.			X1402	47		
n.c.			X1402	48		
n.c.			X1402	49		
n.c.			X1402	50		
n.c.			X1402	51		
n.c.			X1402	52		
n.c.			X1402	55		
n.c.			X1402	56		
n.c.			X1402	57		
n.c.			X1402	58		
n.c.			X1402	59		
n.c.			X1402	60		
n.c.			X1402	61		
n.c.			X1402	62		
n.c.			X1402	63		
n.c.			X1402	64		
n.c.			X1402	65		
n.c.			X1402	66		
n.c.			X1402	67		
n.c.			X1402	68		
n.c.			X1402	69		
n.c.			X1402	70		
n.c.			X1402	71		
n.c.			X1402	72		
n.c.			X1402	73		
n.c.			X1402	74		
n.c.			X1402	75		
n.c.			X1402	76		
VCC33_BYPASS			X1402	77		(VCC33 switched by SB1401)
VCC33_BYPASS			X1402	78		(VCC33 switched by SB1401)
VCC33_BYPASS			X1402	79		(VCC33 switched by SB1401)
VCC33_BYPASS			X1402	80		(VCC33 switched by SB1401)
CPLD_IO_UP0		K2	X1403	3	X302	2
CPLD_IO_UP1		H1	X1403	4	X301	2
CPLD_IO_UP2		H4	X1403	5	X302	3
CPLD_IO_UP3		H3	X1403	6	X301	3
CPLD_IO_UP4		J4	X1403	7	X302	4
CPLD_IO_UP5		J1	X1403	8	X301	4
CPLD_IO_UP6		J3	X1403	9	X302	5
CPLD_IO_UP7		K1	X1403	10	X301	5
JTAG_TDO			X1403	30		
FPGA_IO_UP1	P17	3_31P	X1403	56	X303	6
FPGA_IO_UP0	R22	3_28N	X1403	57	X304	7

FPGA_IO_UP3	R21	3_28P		X1403	58	X303	7
FPGA_IO_UP2	P21	3_32P	X	X1403	59	X304	8 corrected on 2005-07-28
FPGA_IO_UP5	T20	3_26N		X1403	60	X303	8
FPGA_IO_UP4	P22	3_32N	X	X1403	61	X304	9 corrected on 2005-07-28
n.c.				X1404	13		
n.c.				X1404	14		
n.c.				X1404	15		
n.c.				X1404	16		
n.c.				X1404	17		
n.c.				X1404	18		
n.c.				X1404	19		
n.c.				X1404	20		
FPGA_IO_UP16	G3	7_26N		X1404	21		corrected on 2005-07-28
n.c.				X1404	23		
n.c.				X1404	24		
n.c.				X1404	25		
n.c.				X1404	26		
n.c.				X1404	29		
n.c.				X1404	30		
n.c.				X1404	31		
n.c.				X1404	32		
FPGA_IO_UP17	J4	7_29N		X1404	33		corrected on 2005-07-28
n.c.				X1404	35		
n.c.				X1404	36		
n.c.				X1404	37		
n.c.				X1404	38		
n.c.				X1404	39		
n.c.				X1404	40		
n.c.				X1404	41		
n.c.				X1404	42		
n.c.				X1404	43		
n.c.				X1404	44		
FPGA_IO_UP18	P6	6_28P		X1404	45		corrected on 2005-07-28
FPGA_IO_UP19	R2	6_29N		X1404	46		corrected on 2005-07-28
FPGA_IO_UP20	P5	6_31N		X1404	47		
n.c.				X1404	48		
FPGA_IO_UP21	N6	6_33N		X1404	49		
FPGA_IO_UP22	T3	6_26N		X1404	50		
n.c.				X1404	51		
FPGA_IO_UP23	P2	6_32N		X1404	52		
n.c.				X1404	55		
n.c.				X1404	56		
n.c.				X1404	57		
n.c.				X1404	58		
n.c.				X1404	59		
n.c.				X1404	60		
n.c.				X1404	61		
n.c.				X1404	62		
n.c.				X1404	63		
n.c.				X1404	64		
n.c.				X1404	65		
n.c.				X1404	66		
n.c.				X1404	67		
n.c.				X1404	68		
FPGA_IO_UP24	K5	7_33N		X1404	69		
FPGA_IO_UP25	J5	7_31N		X1404	71		
FPGA_IO_UP26	J1	7_32N		X1404	73		

FPGA_IO_UP27	H1	7_28N		X1404	75				
n.c.				X1404	77				corrected on 2005-07-28
n.c.				X1404	78				
BUTTON0			K9						
BUTTON1			L9						
FL_ADDR0	V9		D6						
FL_ADDR1	U10		A3						
FL_ADDR10	AB8		C4						
FL_ADDR11	U7		A1						
FL_ADDR12	V7		C3						
FL_ADDR13	U6		B1						
FL_ADDR14	Y6		E3						
FL_ADDR15	AB11		D1						
FL_ADDR16	AB10		F3						
FL_ADDR17	AA10		G2						
FL_ADDR18	W6		E2						
FL_ADDR19	AA6		E1						
FL_ADDR2	V10		A4						
FL_ADDR20	U11		G1						
FL_ADDR21	Y13		F2						
FL_ADDR22	AB13		E4						
FL_ADDR23	U13		F4						
FL_ADDR24	AA13								
FL_ADDR3	W10		A5						
FL_ADDR4	Y10		C6						
FL_ADDR5	W8		B5						
FL_ADDR6	W9		B4						
FL_ADDR7	V8		D5						
FL_ADDR8	V6		A2						
FL_ADDR9	AA8		B3						
FL_BYTE#	Y16		E10						
FL_CE#	U17		G10						
FL_DATA0	AA14		C9						
FL_DATA1	AB14		A9						
FL_DATA10	AA15								
FL_DATA11	W14								
FL_DATA12	V14								
FL_DATA13	U14								
FL_DATA14	W13								
FL_DATA15	V13								
FL_DATA2	U12		B10						
FL_DATA3	V12		B9						
FL_DATA4	W11		B11						
FL_DATA5	V11		A11						
FL_DATA6	AB9		A12						
FL_DATA7	AA9		A13						
FL_DATA8	U16								
FL_DATA9	AB15								
FL_OE#	AA17		E12						
FL_RP#	V16		F10						
FL_STS	W16		C10						
FL_WE#	Y17		E13						
FPGA_BUF_CCLK			D8						
FPGA_BUF_DONE			C8						
FPGA_BUF_HSWAP_EN			D9						
FPGA_BUF_M0			F12						
FPGA_BUF_M1			G11						

FPGA_BUF_M2		F13
FPGA_BUF_PROG_B		D10
FPGA_CCLK_DIR		G3
FPGA_CPLD_IO0	AB20	K13
FPGA_CPLD_IO1	AA20	K11
FPGA_CPLD_IO2	Y18	J12
FPGA_CPLD_IO3	W18	K10
FPGA_CPLD_IO4	V18	J10
FPGA_CPLD_IO5	AB18	J11
FPGA_CPLD_IO6	AA18	K12
FPGA_CPLD_IO7	AA12	J13
FPGA_CS_B	AA3	E11
FPGA_DOUT_BUSY	Y12	D12
FPGA_INIT_B	W12	D13
FPGA_RDWR_B	Y4	B12
LED0		L8
LED1		K8
OSC1	AB12	D7
OSC1_EN		H13
OSC2	AA11	C7
PSEN_25_12		H12
SWITCH0		M8
SWITCH1		N9
XCF_CE#		G13
XCF_CF#		H11

Note:

The "Diff Pair" flag is set for signals which are routed as a pair on the same layer of the board and are direct neighbours on the connector. This makes these pairs more suitable for differential signalling than the others. However, the Zefant XS3 board is not impedance controlled, for very high speed signalling please use the "Zefant-DDR" FPGA module instead!